



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Group Art Unit: 2811  
Examiner: To be assigned

In Re PATENT APPLICATION Of:

Applicant: Kazutaka SHIBATA )  
Serial No.: 10/727,577 )  
Filed: December 5, 2003 )  
For: Semiconductor device and method for )  
manufacturing same )  
Attorney Ref: AI 254 D1 )

**STATUS REQUEST**

\_\_\_\_\_  
July 22, 2005

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Sir:

Please let us know the status of the above-identified application and when an Action may be expected. If the response to this status request is made by telephone, please as for Tonya Harris (Docket Clerk) at extension 518.

Respectfully submitted,

July 22, 2005  
Date

SMR:klc

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